A 50,000 FT. OVERVIEW OF THE SYSTEM
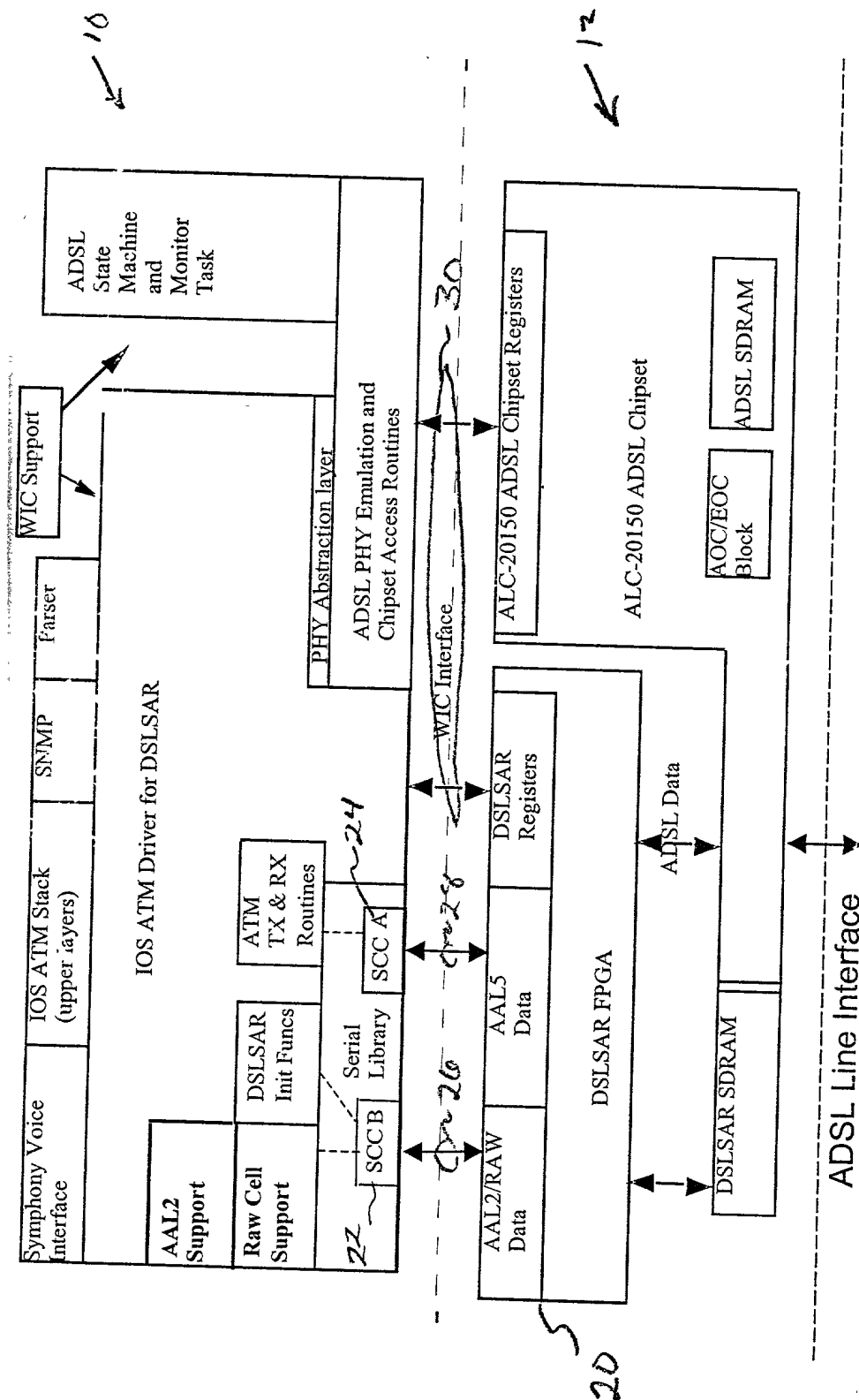
ADSL-WIC

1700 HOST SYSTEM

SCC — ADSL

DSLSAR FPGA

SCC

Serial bus

8 bit WIC Interface bus

860T
IOS ATM
Driver

Fig. 1

Fig. 2

Serial Communication

860 HOST

DSLSAR

TXD → RXD

RTS → CJ

BRG → CLK

CTS=1 → RXD ← TXD

CD ← RTS

CLK ← BRG
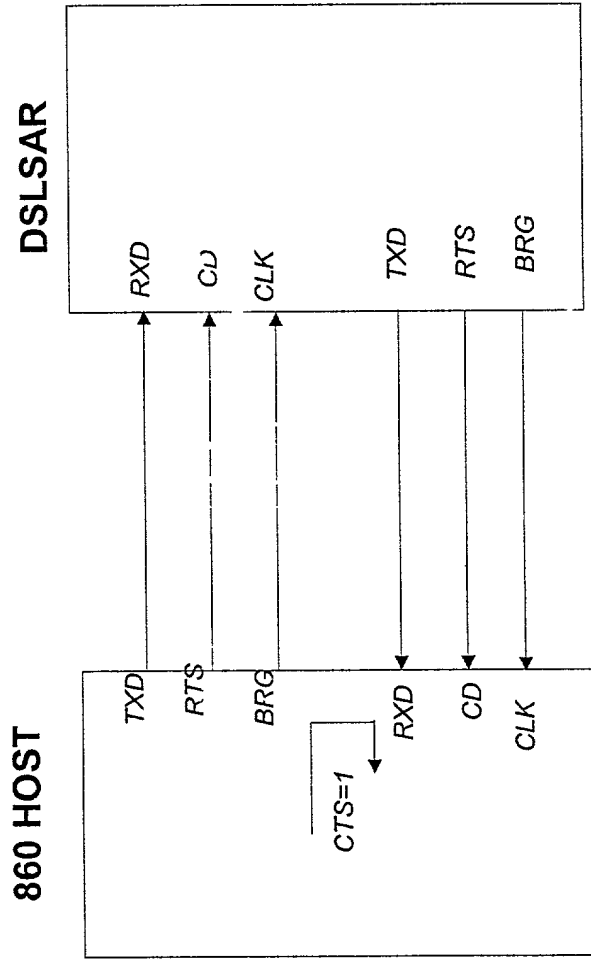
Fig. 3

Opening Flag	Command Opcode	(optional) Parameters	(optional) Data
0x7E	8 bits		

Fig. 4A

Opening Flag	Command Opcode	Length	Address	Data
0x7E	0x01	16 bits	32 bits	Length*8 bits

Fig. 4B

Opening Flag	Command Opcode	Length †	Address
0x7E	0x02	16 bits	32 bits

Fig. 4C

Opening Flag	Command Opcode	Open/Close, Address
0x7E	0x08	32 bits

Fig. 4D

Command	(optional) More Commands	CRC
0x7E, Opcode, etc	0x7E, Opcode, etc	16 bits

Fig. 4E

ID Word	hex 7E80 (16 bits)
Length	4*(N+4) (16 bits)
RPQE	4 longwords
Packet Data	N longwords
CRC-16	16 bits

Fig. 4F

ID Word	hex 7E81 (16 bits)
Length	hex 0038 (16 bits)
Raw Cell	14 longwords
CRC-16	16 bits

Fig. 4G

ID Word	hex 7E82 (16 bits)
Length	hex 0008 (16 bits)
TSQE	2 longwords
CRC-16	16 bits

Fig. 4H

ID Word	hex 7E02 (16 bits)
Length	4*(N+1) (16 bits)
Address	1 longword
Data	N longwords
CRC-16	16 bits

Fig. 4I